IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Application of:

Lap-Ki Au
Daniel Watkins

Serial No.:

10/077,066

Group Art Unit:

2122

Filed:

February 15, 2002

Examiner:

Steelman, Mary J.

For:

System Real-Time Analysis Tool

Atty Docket:

1496.00193 / 01-660

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

3/10/06

Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 3/10/2006

Respectfully submitted,

Henry Groth

Reg. No. 39,696